

APPROVED BY AICTE, AFFILIATED TO JNTUK: KAKINAD, ACCREDITED BY NAAC NH-16, CHAITANYA KNOWLEDGE CITY, GIET CAMPUS, RAJAMAHENDRAVARAM, A.P., INDIA 533296

NH-16, CHAITANTA KNOWLEDGE GITT, GIET GAIN. 4-, 15 TEL: 91-883-6577446, 9133833322, http://www.gietec.ac.in Email: principal t9@yahoo.co.in

naran.ssnraju@gmail.com

Dr. S. Suryanarayana Raju *Ph.D., M.I.S.T.E., M.B.R.S.I.* PRINCIPAL

DATE: 12-12-2017

CIRCULAR

Sub: GEC, RJY A.Y. 2017-2018 FDP Program by the Dept, of CSE - Reg.

All staff and students are hereby notified that the departments of CSE will be hosting a 1-WEEK FDP on Research Methodologies from December 18th to December 23rd, 2017 in the C.V Raman Block Seminar Hall at GIET ENGINEERING COLLEGE, Rajahmundry.

All faculty members are encouraged to take advantage of this chance to attend the workshops.



PRINCIPAL
Principal
GIET ENGINEERING COLLEGE,
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY - 533 296.

GIET ENGINEERING COLLEGE,

NH-16, Chaltanya Knowledge tiley, RAJAHMUNDRY-533 296.

Copy to:

1. VC/GM for kind information

2. E-Circulation to among all the staff.

3. All notice boards

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Registration form

ONE WEEK FACULTY DEVELOPMENT PROGRAM RESEARCH METHODOLOGIES

Name of the Participant:

Name of the Institute:

Address for Correspondence:

Department:

Mail Id:

Phone no:

Signature of the participant:

Dt:



CHIEF PATRON

Sri. KVVS RAJU, Chairman, Chaitanya Group

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Sri. Sasi Kiran Varma, Vice Chairman,

Smt. K.V.V.Lakshmi, Executive Director,

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CHAIRMAN

Dr. S.Suryanarayana Raju ,Principal, GEC

CONVENER

Dr. Y.VENKAT, HOD CSE

CO-CONVENER Principal

GIET ENGINEERING COLLEGE.

Mr. K. Naga Raju, Associate Profi C. Chaitanya Knowledge City, RAJAHMUNDRY-533 296. REGIST RATION FEES: 200Rs

RESOURCE PERSON

Mr. Jaya Babu. Y Professor, Dept of CSE Pragathi Engineering College

ORGANISING COMMITTEE

Dr. K.V.R, Srinivas, Prof. Mathematics Dr. L.N.Murthy, Prof. Chemistry Mr.T.ChandraSekhar, HOD ECE Mr.K.L.N.Murthy, HOD ME Mr. Vamsi, HOD EEE Mr. Kashyap, HOD CE Mr Durga Prasad, Asst.Professor, CSE Ms M.Neelima, Asst.Professor, CSE Mr Phani Kumar, Asst.Professor, CSE Mr.G.Umamahesh, Asst.Professor, CSE Mr.P.Sasi Kumar, Asst.Professor, CSE Ms B.Sai Deepthi, Asst.Professor, CSE Mr.P.Krishnanjanelu, Asst.Professor, CSE Mrs. S. Naga Kumari, Asst.Professor, CSE Mr.R. Lakshmi Kumar, Asst.Professor, CSE

CONTACT:

Mr K.Naga Raju,9989442311, nagarajuknr.k@gmail.com Mr.P.Krishnanjanelu,9032221674, krishna54888@gmail.com



ABOUT COLLEGE

GIET Engineering College is an epitome of excellence in every aspect. Approved by AICTE and affiliated to JNTU, Kakinada (JNTUK) and accredited by NAAC, the institute offers undergraduate programs in diverse engineering streams. It features state-of-the-art facilities and highly qualified and knowledgeable pool of staff members. Apart from the prescribed course curricula, the institute offers an entire educational experience. It has been a pioneer in adopting several pedagogic innovations for the benefit of students. The achievements of the students of this institution in academics as well as placements stand testimony to the fact.

ABOUT DEPARTMENT

Department of Computer Science and Engineering was started in the year 2005. It is offering graduate program B.Tech and post graduate program M.Tech. Department has well qualified and dedicated faculty members. Department has produced good number of graduates well settled in industry and some are also involved in research activities

ABOUT PROGRAM

Research methodology is a way of explaining how a researcher intends to carry out their research. GIET ENGINEERING COLLEGE conducting this 1-week FDP to demonstrate It's a logical, systematic plan to resolve a research problem. A methodology details a researcher's approach to the research to ensure reliable, valid results that address their aims and objectives. It encompasses what data they're going to collect and where from, as well as how it's being collected and analyzed, practitioners, this FDP helps faculty and participants to share their ideas and gain the knowledge in the area of research for further improvement under one roof.

Program Contents

- Research overview and objectives.
- How to prepare the literature survey
- Research methods and Methodologies Policipal
 GIET ENGINEERING COLLEGE
- Process of Research

NH-16, Chaitanya Knowled NGINEERING COLLEGE RAJAHMUNDRY-533 296.

• Sampling in Research Methodologies

ONE WEEK FACULTY DEVELOPMENT PROGRAM ON RESEARCH METHODOLOGIES

18th to 23rd DEC 2017

Organized by

Department of Computer Science and Engineering



GIET





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naran.ssnraju@gmail.com

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

5-DAY FACULTY DEVELOPMENT PROGRAM ON

RESEARCH METHODOLOGIES

ATTENDANCE SHEET

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Principal
GIET ENGINEERING COLLEGI

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naran.ssnraju@gmail.com

Dr. S. Suryanarayana Raju *Ph.D., M.I.S.T.E., M.B.R.S.I.* PRINCIPAL

DATE: 25-12-2017

ONE WEEK FDP ON RESEARCH METHODOLOGIES

Department of Computer Science and Engineering

PROGRAM REPORT

Resource Person

: Mr. Jayababu Yasarapu

Professor, Dept. of CSE

Pragati Engineering College

Duration

: One Week

Dates

: 18-12-2017 to 23-12-2017

No.Of Participants

: 40

Description

: The ceremony was launched on December 18, 2017 by the chairman, Sri.K.V.V.Satyanarayana Raju garu, Principal Dr.SSN Raju garu, HOD Of CSE Dr.Y.Venkat garu, and all the Computer Science and Engineering teaching staff.

On the same day, Dr. PSVVS .Ravi Kumar, the resource person, gave an introduction to research and an overview of research.

The second day session began at 10 a.m., with the resource person delivering a lecture on the aims of research, a literature review, and experimental techniques.

On the third day, the session began at 10 a.m. as normal, with a lecture on the many forms of research and the difference of research techniques and methodology.

Perncipal

GIET ENGINEERING COLLEGE.

NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296. On the fourth day, the session began at 10 a.m. as normal, with a presentation on the Research Process, which includes research problems and comprehensive research.

On the fifth day, the session began at 10 a.m. as normal, with a lecture on the Research Process, which includes determining the sample design, data collecting, research execution, data analysis, and hypothesis testing.

On the final day of the programme, I gave a talk on different forms of sampling in research methods.

In the valedictory event at the end of the session, the resource person was honoured by the principal and the heads of all departments at GIET Engineering College.



Principal
GIET ENGINEERING COLLEGE,
NH-16, Chaltanya Knowledge City,
RAJAHMUNDRY-533 296.

CONVENER



Co-ordinator

GIET ENGINEERING COLLEGE

Approved by AICTE, Affiliated to JNTUK, Kakinada, Accredited by NAAC NH-16, Chaitanya Knowledge City, GIET Campus, Rajamahendravaram-533296, A.P., India Phone: 9652929111, Website: https://www.gietec.ac.in, email: principalgec@giet.ac.in.

Principal

Certificate

This is to certify that Dr/Mr/Mrs	DONIV VIC-U	of	BITS	TO STATE OF THE ST
participated in a Five Day Faculty	Development Program on "	Research Me	ethodologies'	' held from
18th to 23th December 2017, O	rganised by Department of	Computer	Science and	Engineering,
GIET Engineering College.	Principal GIET ENGINEERING COLLECT			e 8
	GIET ENGINEERING COLLECT			

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Dr. S. Suryanarayana Raju Ph.D., M.I.S.T.E., M.B.R.S.I. PRINCIPAL

CIRCULAR

2017-18/GEC

Date: 19/02/2018.

ORIGINATOR	ME & CIVIL ENGINEERING
CIRCULATED TO	Department of - CSE, ECE, EEE, MBA, HBS,
	LIBRARY,

All staff are hereby notified that the departments of ME &CIVIL will be holding a Five Day Faculty Development Program on Advanced NDT Techniques & Applications in Industry From 19/02/2018 to 23/02/2018, every day from 10:00 AM to 4:30 PM, at the C.V Raman Block Seminar Hall

Venue: C.V Raman Block Seminar Hall

Date: 19-02-2018

Coordinator

Principal GIET ENGINEERING COLLEGE. NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296.

U Subbalakshmi, Assot.Prof (Ph.D)

Head, Civil Engg. Dept.



K L N Murthy, Sr. Asst.Prof (Ph.D) Head, Mechanical Engg. Dept.

GIET ENGINEERING COLLEGE. NH-16, Chaitanya Knowledge City. RAJAHMINDRY-522 008

RESOURCE PERSONS

Resource Persons for the FDP will be highly experienced faculty members from reputed institute like IITs, NITs and Central Universities.

ABOUT MECHANICAL ENGINEERING DEPT.

The Department of Mechanical Engineering has evolved into one of the finest in terms of teaching curriculum and methodology supported by a well-organized and adequately funded research program. The Department has a very well-established B. Tech program complemented by two M. Tech, programs in Mechanical System Design and Industrial Tribology and Maintenance Management. The department is, perhaps, the most versatile in terms of the range of specializations of its faculty members and a well experienced support staff.

DEPARTMINIATION

To nurture mechanical engineers with passion for professional excellence, ready to take global challenges and to serve the society with high human values.

DEPARTMENT MISSION

To provide facilities and infrastructure for academic excellence in the field of mechanical engineering.

To inculcate in the student the passion for understanding professionalism, ethics, safety, sustainability and then actively contribute in the society.

To muture creativity and encourage innovative solutions to real life challenging problems in mechanical engineering students.

Registration is open

WHO CAN ATTEND?

The faculty members of the AICTE approved institutions, research scholars, PG Scholars, participants from Government, Industry, CBSE Teachers and staff of host institutions.





GIET ENGINEERING COLLEGE, NH-16, Chaitanya Knowledge City, RAJAHMUNDPY 503 226.

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About the FDP

An intensive online FDP on Non-destructive testing and evaluation will be offered by the Department of Mechanical Engineering, National Institute of Technology, Srinagar. It is sponsored by AICTE, Training & Learning Academy. The FDP is designed to cater the needs of academicians, scientists from R & D houses, Labs and practicing engineers from industries. This programme will be specifically useful for persons who are concerned with training/teaching, research, and industrial applications of NDT&E.

FDP Objectives

Fundamental of ultrasonic testing and phased array ultrasonic NDT&E of welded joints related topics would be discussed in detail. FDP would also focus on latest advances and applications of NDT techniques.

FDP Contents

- Concept of Non-destructive testing and evaluation
- Ultrasonic Testing and phased array ultrasonic NDT&E of welded joints
- NDT applications in aerospace and oil refineries.
- Latest advances and Application of NDT

FDP Highlights

- Seats are limited and the selection will be done on first-cumfirst-serve basis
- & E-Cerificate will be provided to the Registered Participants
- * Dates and Timings:
 - o(19 Feb 23 Feb 2018)
 - Forenoon: 10 00 TO 01:00 (Session-1)
- o Afternoon: 02 00 TO 04:30 (Session-2)

CHIEF PATRON

Sri. K.V.V. Satyanarayana Raju Chairman, GIET Institutions

PATRON

Sri. Sasi Kiran Varma Kalidindi
Vice Chairman, GIET Institutions
Smt. Lakshmi Sasi Varma Kalidindi
Executive Director, GIET Institutions
Dr. P. Subba Raju,
General Manager, GIET Institutions
Dr. T.V. Prasad,
Director QA, GIET Institutions

CHAIRMAN

Dr.S.Surya Narayana Raju Principal, GIET Engineering College.

CONVENER

K L N Murthy, Sr. Asst.Prof (Ph.D) HOD-ME, GEC.

COORDINATOR

U Subba Lakshmi, Assot.Prof (Ph.D) HOD-CIVIL, GEC.

About

GIET ENGINEERING COLLEGE

The college is located at Chaitanya Nagar on NH-16 on the outskirts of Rajamahendravaram, in GIET campus. GIET Engineering College is an epitome of excellence in every aspect. Institute is Approved by AICTE and affiliated to JNT University Kakinada (JNTUK). The Institute got accredited by NAAC. The institute offers undergraduate programs in diverse engineering streams. It features state-of-the-art facilities and highly qualified and knowledgeable pool of staff members.

VISION OF THE INSTITUTE

To evolve as a Premier Engineering Institution in the Country with its continues strive for excellence in Education, Research and Technological Services.





5 DAYS

Faculty Development Programme (FDP)

On

Advanced NDT Techniques & Applications in Industry

- -(19 Feb - 23 Feb 2018)



Organized by

Deapartment of ME & Civil Engineering
GIET ENGINEERING COLLEGE
NH-16, Chaitanya Knowledge City,

NH-16, Chaitanya Knowledge City, Rajahmundry-533 296, EAST Godavari District, Andhra Pradesh, India ipal NG COLLEGE, Knowledge City DRY-533 296.



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DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program on

Advanced NDT Techniques & Applications in Industry

SCHEDULE SHEET

DAY	TIME	TOPIC
	10:00 TO 11:30	Inauguration
DAY 1	11:30 TO 11:45	Break
2711	11:45 TO 01:00	Introduction
	01 : 00 TO 04 : 30	Phased Array Ultrasonic Testing (Paut)
DAY 2	10:00 TO 01:00	Time Of Flight Diffraction Testing (Tofd)
	02 : 00 TO 04 : 30	Internal Rotating Inspection System (Iris)
	10:00 TO 01:00	Visual Inspection (Vt) &
DAY 3		Ultrasonic Flaw Detection Testing (Ut)
	02:00 TO 04:30	Positive Alloy Material Identification (Pami) &
		Painting & Coating Inspection
DAY 4	10:00 TO 01:00	Magnetic Particle Testing (Mpi) &
DAT 4		Liquid Penetrant Testing (Lpt)
	02 : 00 TO 04 : 30	Wire Rope Testing & Remote Visual Inspection
		(RVI)
DAY 5	10:00 TO 11:00	Internal Rotating Inspection System (Iris) &
DAIS		Near Filed Testing (Nft)
	11:00 TO 12:30	Valedictory

ENGINEERING

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Principal
GIET ENGINEERING COLLEGI
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

KONVENOR

GIET ENGINEERING COLLEGINH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296.



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DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program Advanced NDT Techniques & Applications in Industry

Teaching Staff Attendance Sheet

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Principal GIET ENGINEERING COLLEGE. NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296.

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5 DAYS Faculty Development Program
On
Advanced NDT Techniques & Applications in Industry

Teaching Staff Attendance Sheet

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COORDINATOR

Principal
GIET ENGINEERING COLLEGE,
NH-16, Chaltanya Knowledge City,
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CONVENDR
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Advanced NDT Techniques & Applications in Industry

Teaching Staff Attendance Sheet

S.NO	NAME OF THE FACULTY	19/02/2018	20/02/2018	21/02/2018	22/02/2015	T
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COORDINATOR

Principal
GIET ENGINEERING COLLEGE
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

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APPROVED BY AICTE, AFFILIATED TO JNTUK: KAKINAD, ACCREDITED BY NAAC NH-16, CHAITANYA KNOWLEDGE CITY, GIET CAMPUS, RAJAMAHENDRAVARAM, A.P., INDIA 533296 TEL: 91-883-6577446, 9133833322, http://www.gietec.ac.in Email: principal t9@yahoo.co.in

DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program
On
Advanced NDT Techniques & Applications in Industry

OBJECTIVES

- Fundamental of ultrasonic testing and phased array ultrasonic NDT&E of welded joints related topics would be discussed in detail.
- FDP would also focus on latest advances and applications of NDT techniques.
- The faculty are to be exposed to the concepts of various NDE techniques using radiography, ultrasonic's, liquid penetrates, magnetic patches and Eddy currents
- They will learn basic principles of these methods and will be able to select a testing process

• They will understand the advantages and disadvantages of these techniques.

Kly

THEINEERING COLLEGE *

PRINCIPAL
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DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program
On
Advanced NDT Techniques & Applications in Industry

Venue: C.V Raman Block Seminar Hall

Date: 24 /02 /18

"Summary Report"

Description:

The 5-day FDP programme on "Advanced NDT Techniques & Applications in Industry" was conducted for the academic staff of several colleges, including GIET Engineering College, and will run from 19-02-2018 to 23-02-2018. The FDP's resource person was Mr. P V P Prasad, Structural Engineer ,Vijayawada . Faculty members have gained in-depth knowledge of upcoming demands in many research fields, criteria to be taken into account when publishing papers in reputable journals, and various cutting-edge teaching techniques.

Programme Outcomes:

A major goal of this study was to develop, evaluate and implement the use of a commercially available device for nondestructive structural evaluation of pavements.

- Concept of Non-destructive testing and evaluation
- Ultrasonic Testing and phased array ultrasonic NDT&E of welded joints
- NDT applications in aerospace and oil refineries.
- Latest advances and Application of NDT the FDP results are significantly related to Benkelman Beam Test;

CONVENOR



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Resource person demonstrating about Advanced NDT Techniques & Applications in Industry





Principal
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Certificate of Appreciation

This is to certify that Mr/Ms/Dr. <u>K. Sri. Harika</u> has participated A 5-Day Faculty

Development Program on "Advanced NDT Techniques & Applications in Industry held from 19 th - 23 rd Febrauary 2018, organized by Civil Engineering Department & Mechanical Engineering Department, GIET Engineering College.

Convenor

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Certificate of Appreciation

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> Dt: 28/03/2018, Rajahmundry.

Dr. S. Suryanarayana Raju Ph.D., M.I.S.T.E., M.B.R.S.I. PRINCIPAL

CIRCULAR

All Teaching and Non Teaching Staff are hereby notified that the department of Humanities and Basic sciences (HBS), Management Studies (MBA) will be holding a Five Day Faculty Development Program (FDP) on Personality Development and Soft Skills From 02/04/2018 to 06/04/2018.

Venue

: C.V Raman Block Seminar Hall

Date & Time: 02/04/2018 & 10:00 AM to 4:30 PM

Coordinator

Dr. KVR Srinivas

HOD & Professor

Department of HBS

Convener

Dr. K. Satish

HOD & Professor

Department of MBA

16, Chaitanya Knowledge City, Chaitanya Knowledge City, RAJAHMUNDRY-533 296.

INSTITUTION

GIET Engineering College is Sponsored by "Koundraya Educational Society" Rajamahendravaram, with a concrete plan to offer excellence in technical education and to meet the present as well as the changing needs of the corporate sector. Technocrats, Academics and Philanthropists of Koundraya Educational Society" formed in GIET in the year 2005.



The Department of Humanities and Basic sciences was established in the year 2005 with an intake of 270 students. The significant infrastructural design, wellequipped laboratories & hi-tech computers with high-speed internet facility with all qualified staff members ensure better quality of education delivered from our department. Humanities and Basic Sciences is a dynamic and exciting area provides excellent career opportunities in various sectors of the society.

ORGANISING COMMITTEE CHIEF PATRON:

Dr.KVV Satyanarayana Raju Chairman, Chaitanya Groups

PATRON

Sri K Sasi Kiran yarma

Managing Director, GIET Institution...
Smt K Lakshmi Sasi varma
Executive Director, GIET Institutions

CO PATRON:

Dr. P Subba Raju

Genaral Manager,

GIET Institutions

CHAIRMAN:

Dr. S Suryanarayana Raju Principal,GEC

CONVENER:

Dr. K.Satish

HOD, Dept of MBA

COORDINATOR: Dr. K V R Srinivas

HOD, Dept of HBS

COMMITTEE MEMBERS Principal East Godavari (Dist.) Committee Members Principal East Godavari (Dist.) Committee Members Principal East Godavari (Dist.) Committee Members Principal East Godavari (Dist.)

Mrs. M Madhavi Asst. Professoraitanya Knowledge City RAJAHMUNDRY 533 19RESO

Mr. MMK Raju Asst. Prof. Mrs.DVV VENI, Asst. Prof.

Mr. D samuel, Asst. Prof

FIVE-DAY FACULTY DEVELOPMENT PROGRAM

On

Personality Development and Soft Skills

 2^{nd} -6^{th} April, 2018.

Organized by

Department of HBS & MBA



GIET ENGINEERING COLLEGE Affiliated to JNTUK, Kakinada, Rajamahendravaram-533296, East Godavari (Dist.)

PRESOURCE PERSON:

Dr. Chakrapani Kakinada, Assoc.Professor in English, Pragathi Engineering College (A) ,Kakinada.

OBJECTIVE:

To Enrich Self-confidence.

To Develop Positive attitude and

Emotionalintelligence

To Enrich social area and flexibility and friendliness.

To Develop effective communication skills

.To Enrich motivation to wards aptitude andbehavior

.To Develop self management .To Develop self motivation. To Develop Time management.

RESOURCE PERSON:

The resource person those who will be conducting the program are the eminent TENGIA academicians from reputed Institutions.//

ELIGIBILITY:

Faculty members of Engineering / Polytechnic colleges approved by AICTE /Researchers / Practicing Engineers/PG and UG Final Year students are eligible.

COORDINATOR:

Mr. K Rajendra Prasad.

Assistant Professor, Dept of HBS

GIET Engineering College

Rajamahendravaram -533296

East Godavari (Dist.), Andhra Pradesh

Mobile No: 8919195058,8977457760

Email: kolli.rajendra.kolli@gmail.com

REGISTRATION FOR FIVE -DAY FACULTY **DEVELOPMENT PROGRAM**

On

Personality Development and Soft Skills

2nd -6th April, 2018.

- 1. Participation Name:
- 2. Qualification:
- 3 .Department:
- 4. Organization:
- 5. Address for Communication
- 6. Phone No:
- 7. E-Mail:
- 8. Accommodation required: Yes/No

Date:

Signature of Participant

RDING & LODGING:

Prucipal GIET ENGINEERING COLLEREgistration Fee: 200/-

NO accommodation will not be MINDRY - 33 296.

provided by college.



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TEL: 91-883-6577446, 9133833322, http://www.gietec.ac.in Email: principal t9@yahoo.co.in

DEPARTMENT OF HBS & MBA

FIVE-DAY Faculty Development Program

on

Personality Development and Soft Skills Schedule Sheet

DAY	TIME	TOPIC
	10:00-11:30	Inauguration
Day -1	11:30 - 11:45	Tea Break
	11:45 - 01:00	Introduction
	02: 00 - 04 : 30	To Enrich Self-confidence
Day- 2	10: 00 - 01 : 00	To Develop Positive attitude
	02:00-04:30	To Enrich Emotional intelligence
9	10:00 - 01 : 00	To Develop Social grace, flexibility
Day- 3	02:00-04:30	To Enrich Friendliness
Day- 4	10: 00 -01 : 00	To Develop Effective communication skills
	02; 00 - 04 : 30	To Enrich Motivation to wards aptitude and behavior
Day -5	10:00-01:00	To Develop Self management, Self motivation and Time Management
	02:00-04:30	valedictory

Coordinator

Principal
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RAJAHMUNDRY-533 296.

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DEPARTMENT OF HBS & MBA

FIVE- DAY Faculty Development Program

On

Personality Development and Soft Skills

ATTENDANCE SHEET

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Coordinator

Principal

GIET ENGINEERING COLLEGE

* REJAMINENDRAVIO NH-16, Chaitanya Knowledge City RAJAHMUNDRY-533 296.

Convener



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DEPARTMENT OF HBS & MBA

FIVE- DAY Faculty Development Program

On

Personality Development and Soft Skills

ATTENDANCE SHEET

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Coordinator

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E. Safish Convener

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DEPARTMENT OF HBS & MBA

FIVE-DAY Faculty Development Program

On

Personality Development and Soft Skills

OBJECTIVES

- To Enrich Self-confidence.
- > To Develop Positive attitude.
- To Enrich Emotional intelligence.
- > To Develop Social grace, flexibility.
- To Enrich Friendliness and.
- To Develop Effective communication skills.
- To Enrich Motivation to wards aptitude and behavior.
- To Develop Self management, Self motivation and Time Management.

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Date: 07/04/2018

DEPARTMENT OF HBS & MBA FIVE-DAY Faculty Development Program

On

Personality Development and Soft Skills

Summary Report

Description:

FIVE-day FDP program on "**Personality Development and Soft Skills** "was conducted for the academic staff of several colleges, including GIET Engineering College from 02/04/2018 to 06/04/2018. The FDP's resource person was Dr.Chakrapani Kakinada, Associate Professor in English, Pragathi Engineering College(A), Kakinada. Faculty members have gained in-depth knowledge of upcoming demands in many research fields, criteria to be taken into account when publishing papers in reputable journals, and various cutting-edge teaching techniques.

Program Outcomes:

A major goal of this study was to develop, evaluate and implement the use of a commercially availabledevice for nondestructive structural evaluation of pavements.

- > Develop and nurture a deep understanding of personal motivation.
- ➤ Develop an understanding of and practice personal and professional responsibility.

 Demonstrate knowledge of personal beliefs and values and a commitment to continuing personal reflection and reassessment.

Coordinator

と・多い 石家 Convener

Principal
GIET ENGINEERING COLLEGE
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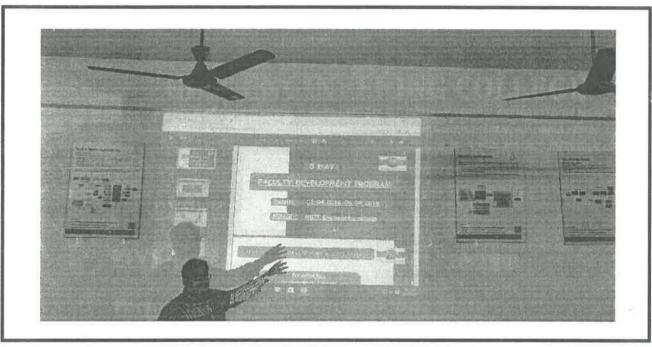
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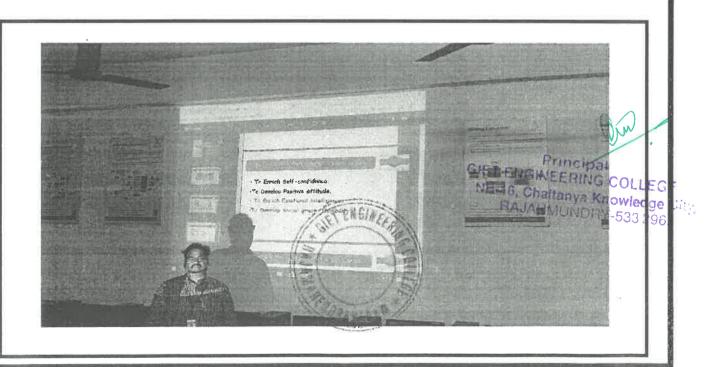


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DEPARTMENT OF HBS & MBA

FIVE-DAY Faculty Development Program On Personality Development and Soft Skills







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Certificate

This is to certify that Dr/Mr/Mrs. N. ESWahaRao

of GIET Engineering Glage

participated a Five Day Faculty Development Program on "Personality Development and Soft Skills"

held from 2 nd to 6th April 2018, Organised by Department of Humanities and Basic Sciences,

GIET Engineering College.

Principal GIET ENGINEERING O

NH-16, Chaitanya Knowld

Co-ordinator

GIET ENGINERB VOZOLLEGE NH-16, Chaltanya Knowledge City RAJAHMUNDRY-533 296.



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Certificate

This is to certify that Dr/Mr/Mrs. K Rajendra prasad Babu of GIET Engineering College

participated a Five Day Faculty Development Program on "Personality Development and Soft Skills"

held from 2 nd to 6th April 2018, Organised by Department of Humanities and Basic Sciences,

MARENDRA

GIET Engineering College.

GIET ENGINEERING COLEGE.
NH-16, Chaitanya Knowledge City.

RAJAHMUNDRY-53

Co-ordinator

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Certificate

This is to certify that Dr/Mr/Mrs. R. Vipya Babu

Of GIET Engineering College

participated a Five Day Faculty Development Program on "Personality Development and Soft Skills"

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GIET Engineering College.

Co-ordinator NE-16. Chaltanya Knowled Will

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Certificate

This is to certify that Dr/Mr/Mrs. D. Jacob

Of GIET Engineering College

participated a Five Day Faculty Development Program on "Personality Development and Soft Skills"

held from 2 nd to 6th April 2018, Organised by Department of Humanities and Basic Sciences,

GIET Engineering College.

Co-ordinator



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Certificate

This is to certify that Dr/Mr/Mrs. G. Vipy

Of GIET Engineeving Tollege

participated a Five Day Faculty Development Program on "Personality Development and Soft Skills"

held from 2 nd to 6th April 2018, Organised by Department of Humanities and Basic Sciences,

GIET Engineering College.

Co-ordinator

Principal ENGINEERING COLLEGE Chaitanya Knowledge City,

GIET ENGINEERING

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TEL: 91-883-6577446, 9133833322, http://www.gietec.ac.in Email: principal t9@yahoo.co.in

Date: 01-09-2018

Dr. S. Suryanarayana Raju

Ph.D., M.I.S.T.E., M.B.R.S.I.

PRINCIPAL

CIRCULAR

All faculty and students are hereby informed that the departments of ECE and EEE organizing a 5 day FDP on Real Time Embedded Systems and IoT, Its Applications from 10-09-2018 to 14-09-2018 in GIET ENGINEERING COLLEGE, Rajahmundry at Seminar hall, C.V Raman Block.

We are requesting all faculty members to attend the session to make it grand success..

Venue: seminar hall, C.V Raman Block

Date of programme: 10-09-2018.



GIET ENGINEERING COLLECTION COLLE

Copy To

1. All HoDs(for information)

2. Notice Board, IQAC,

3. File

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Principal
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INSTITUTION

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The Department of Humanities and Basic sciences was established in the year 2005 with an intake of 270 students. The significant infrastructural design, well-equipped laboratories & hi-tech computers with high-speed internet facility with all qualified staff members ensure better quality of education delivered from our department. Humanities and Basic Sciences is a dynamic and exciting area that provides excellent career opportunities in various sectors of the society.

ORGANISING COMMITTEE CHIEF PATRON

Dr.KVV Satyanarayana Raju Chairman, Chaitanya Groups

PATRON

Sri K Sasi Kiran varma

Managing Director, GIET Institition..

Smt K Lakshmi Sasi varma

Executive Director, GIET Institutions

Co Patron

Dr. P Subba Raju

Genaral Manager, GIET Institutions

Chairman

Dr. S Suryanarayana Raju

Principal, GEC

CONVENOR

Mr T.Chandhra sekhar

HOD, Dept of ECE

CO CONVENOR

Mr. B. Vamsi Krishna

Asst. Professor Dept of EEE

COMMITTEE MEMBERS

Mrs. K.Vijaya kumari Asst. Prof,

Mr. P.Siva krishna Asst. Prof.

Mr.B.Samuel , Asst. Prof

FIVE-DAY FACULTY DEVELOPMENT PROGRAM

On

Real Time Embedded Systems and IoT, Its Applications 10th -14th September, 2018

Organized by

Department of ECE &

EEE



GIET ENGINEERING COLLEGE Affiliated to JNTUK, Kakinada, Rajamahendravaram-533296 East Godaya

Principal
GIET ENGINEERING COLLEGI
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

OBJECTIVE

Internals of Real-Time operating system and the fundamentals of RTOS
The basic concepts of an embedded system .— Fundamental issues in hardware software co-design were presented and explained -. Embedded system implementation and testing tools are introduced and Familiarise with the different IDEs for firmware development and Analyze applications of IoT in real time scenario_

RESOURCE PERSON:

S. Bharathi Devi, Assistant professor, Department of ECE,RIET Engineering College

The resource person those who will be conducting the program are the eminent academicians from reputed Institutions.

ELIGIBILITY

Faculty members of Engineering /
Polytechnic colleges approved by AICTE /
Researchers / Practicing Engineers/ PG and
UG Final Year students are eligible.

CONVENOR

Mr T.Chandhra sekhar

GIET Engineering College

Rajamahendravaram -533296

East Godavari (Dist.), Andhra Pradesh

Mobile No: 9949806418

Email ece.gec@giet.ac.in

BOARDING & LODGING

Registration fee: Rs 250/-

☐ NO accommodation will not be provided by college.

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REGISTRATION FORM FIVE-DAY FACULTY DEVELOPMENT PROGRAM

On

Real Time Embedded Systems and IoT, Its Applications

10th - 14th september, 2018.

Participant's Name:

Qualification:

Department:

Organization:

Address for Communication

TO1 ...

Registration fee: NIL

Accommodation required: Yes/No

Signature of

Date:

Participant

SPONSORSHIP CERTIFICATE

This is to certify that Mr. / Ms------is an employee of our institution since-----and is hereby sponsored to attend the workshop at GIET, Rajamahendravaram, during the period $6^{th} - 10^{th}$ January, 2020.

Place:

Signature of HOD



APPROVED BY AICTE, AFFILIATED TO JNTUK: KAKINAD, ACCREDITED BY NAAC
NH-16, CHAITANYA KNOWLEDGE CITY, GIET CAMPUS, RAJAMAHENDRAVARAM, A.P., INDIA 533296
TEL: 91-883-6577446, 9133833322, http://www.gietec.ac.in Email: principal t9@yahoo.co.in

Department of ECE & EEE

5-Day FDP on Real Time Embedded Systems and IoT, Its Applications

SCHEDULE SHEET

		SCHEDULE SHEET						
DATE	TIME	TOPIC						
	10 AM TO 11.30PM	Inauguration						
	11.30AM TO 11:45PM	Break						
	11:45AM TO 1:00PM	Introduction						
DAY1	2PM TO 4.30 PM	Introduction to Embedded Systems and						
		Real Time Operating Systems, Real time kernal						
	10 AM TO 1PM	Demo on Programmable SoC (Zynq – SoC), FPGA &						
DAY 2		SoC Based Embedded Systems Designs						
	2PM TO 4.30 PM	Sensors and IoT Cloud, Socket Programming.						
	10 AM TO 1PM	Node Red over IBM Bluemix, Hardware and Software						
		Co-design						
	2PM TO 4.30 PM	Emerging Trends in Embedded System.						
DAY3		Embedded Networking Protocols						
	10 AM TO 1PM	Hands on Training Using Keil IDE for 8051 Microcontroller						
DAY 4	2PM TO 4.30 PM	Design of Multiplier using Vedic Mathematics.						
	10 AM TO 1PM	Raspberry Pi Architecture						
		E-mail Controlled Home Automation						
DAY5	2PM TO 4.30 PM	Valedictory						

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TEL: 91-883-6577446, 9133833322, http://www.gietec.ac.in Email: principal t9@yahoo.co.in

5-Day FDP on Real Time Embedded Systems and IoT, Its Applications

Attendance Sheet

CNO	NAME OF THE FACULTY	ATTENDANCE							
SNO		10-09-2018	11-09-2018	12-09-2018	13-09-2018	14-09-2018			
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5-Day FDP on Real Time Embedded Systems and IoT, Its Applications

Programme Objectives

- 1. The basic concepts of an embedded system are introduced.
- 2. The various elements of embedded hardware and their design principles are explained.
- 3. Different steps involved in the design and development of firmware for embedded systems is elaborated.
- 4. Internals of Real-Time operating system and the fundamentals of RTOS based embedded firmware design is discussed.
- 5. Fundamental issues in hardware software co-design were presented and explained.
- 6. Familiarise with the different IDEs for firmware development for different family of processors/controllers and embedded operating systems.
- 7. Embedded system implementation and testing tools are introduced and discussed.
- 8. Analyze various protocols for IoT.
- 9. Design a PoC of an IoT system using Rasperry Pi/Arduino iv.
- 10. Apply data analytics and use cloud offerings related to IoT.
- 11. Analyze applications of IoT in real time scenario

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HAA TEL: 91-883-6577446, 9133833322, http://www.gietec.ac.in Email: principal t9@yahoo.co.in

Department of ECE & EEE

5-Day FDP on Real Time Embedded Systems and IoT, Its Applications Summary Report

Date: 16-09-2018

Description:

5-Day FDP Programme on "Real Time Embedded Systems and IoT, Its Applications" was conducted for the academic staff of several colleges, including GIET Engineering College, and will run from 10-09-2018 to 14-09-2018. The FDP's resource person was . S. Bharathi Devi, Assistant professor, Department of ECE,RIET Engineering College. Faculty members have gained in-depth knowledge of upcoming demands in many research fields, criteria to be taken into account when publishing papers in reputable journals, and various cutting-edge teaching techniques.

On 10-09-2018 function was inaugurated by the chairman sri.K.V.V.Satyanarayana Raju garu, Principal.S.Surya Narayan Raju garu, HOD Of ECE T.Chandra sekhar garu & HOD Of EEE B.Vamsi krishna garu and all the teaching staff of Electronics and Communication Engineering, Electronics and Electrical Engineering.

On the same day Session was taken by the Resource person . S. Bharathi Devi has given introduction about Introduction to Embedded Systems and Real Time Operating Systems,Real time kernal.

On the 2nd day session was started at 10AM and the resource person given his lecture on Programmable SoC (Zynq – SoC), FPGA & SoC Based Embedded Systems Designs

On the 3rd day Session was started usual time at 10AM and given the lecture on Emerging Trends in Embedded System, Embedded Networking Protocols.

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On the 4th day Session was started usual time at 10AM and given the lecture on Design of Multiplier using Vedic Mathematics.

On the 5th day Session was started usual time at 10AM and given the lecture on Raspberry Pi Architecture, E-mail Controlled Home Automation.

At the end of the session in valedictory function, resource person felicitated by the Principal and HOD's of all departments in GIET Engineering College.

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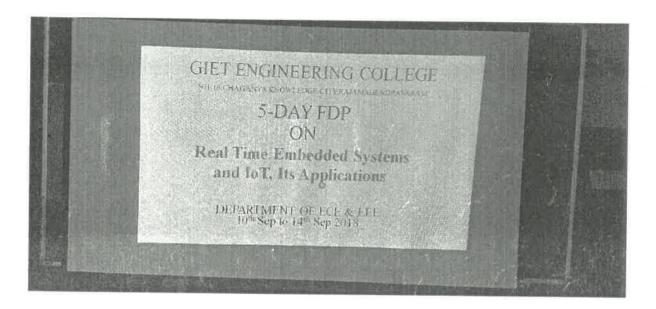
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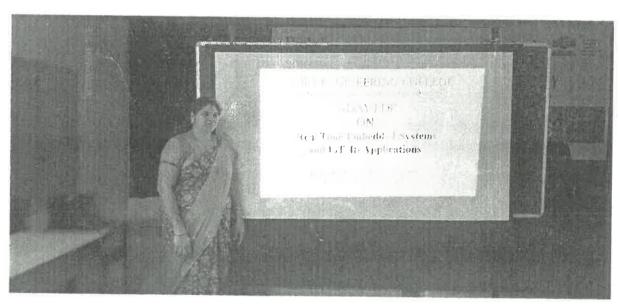


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5-Day FDP on Real Time Embedded Systems and IoT, Its Applications





Resource person Demonstrating about Real Time Embedded Systems and IoT, Its Applications

B. lleit

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GIET ENGINEERING COLLEGE,
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RAJAHMUNDRY-533 296.

Dt: 27/10/2017

Dr. S. Suryanarayana Raju

Ph.D., M.I.S.T.E., M.B.R.S.I.

PRINCIPAL

CIRCULAR

All faculty and students are hereby informed that the departments of ECE and EEE organizing a 6 day FDP on **VLSI Digital Signal Processing** from 06/11/2017 to 11/11/2017 in GIET ENGINEERING COLLEGE, Rajahmundry at Seminar hall, C.V Raman Block.

We are requesting all faculty members to attend the session to make it grand success.

Venue: Seminar hall, C.V Raman Block

Date of programme: 06-11-2017



GIET ENGINEERING COLLEGE NH-16, Chairanya Knowledge City, RAJAHMUNDRY-533 296,

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Department of ECE & EEE

2017 1

6-Day FDP on VLSI Digital Signal Processing

SCHEDULE SHEET

DATE	TIME	TOPIC
	10 AM TO 11.30PM	Inauguration
	11.30AM TO11:45PM	Break
DAY1	11:45AM TO 1:00PM	Introduction
	2PM TO 4.30 PM	VLSI for Health Monitoring Systems
	10 AM TO 1PM	VLSI DSP –Logic optimization
DAY2	2PM TO 4.30 PM	VLSI for Wireless application
	10 AM TO 1PM	VLSI Wireless applications -Case studies
DAY3	2PM TO 4.30 PM	Graphical representation of DSP Algorithm
	10 AM TO 1PM	High level transformations of SFG and DFG
DAY 4	2PM TO 4.30 PM	Retiming a DFG, examples from DSP
	10 AM TO 1PM	Bit serial, digit serial.
DAY5	2PM TO 4.30 PM	Concept of folding, Area and power minimization
DAY6	10 AM TO 1PM	Bit level architectures, Examples, Homer's rule
	2PM TO 4.30 PM	Valedictory
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6-Day FDP on VLSI Digital Signal Processing

Programme outcomes

- 1. Understand the properties of MOS active devices and simple circuits configured when using them and the reason for such encumbrances as ratio rules by which circuits can be interconnected in silicon.
- 2. Three sets of design rules with which nMOS and CMOS designs may be fabricated.
- 3. Understand the scaling factors determining the characteristics and performance of MOS circuits in silicon technology.
- 4. Know about scaling of MOS circuits v. Know about FPGA design, synthesis and different case studies
- 5. Apply the difference equations concept in the Discrete time systems
- 6. Use the FFT algorithm for solving the DFT of a given signal
- 7. Design a Digital filter (IIR) from the given specifications Realize the IIR structures from the designed digital filter.
- 8. VLSI Digital Signal Processing assumes importance from the point of view of wide spread use of digital signal processors for number of applications in various fields. This FDP aims to provide a broad coverage of techniques for designing efficient DSP architectures.

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Department of ECE & EEE

6-Day FDP on VLSI Digital Signal Processing

Summary Report

Date: 13-11-2017

Description:

6-Day FDP Programme on "VLSI Digital Signal Processing" was conducted for the academic staff of several colleges, including GIET Engineering College, and will run from 06-11-2027 to 11-11-2017. The FDP's resource person was P.Gangadhar, Department of ECE, Assistant professor, ISTS WOMENS Engineering College, East Ganugudem. Faculty members have gained in-depth knowledge of upcoming demands in many research fields, criteria to be taken into account when publishing papers in reputable journals, and various cutting-edge teaching techniques.

On 06-11-2017 function was inaugurated by the chairman sri.K.V.V.Satyanarayana Raju garu, Principal .S.S N Raju garu, HOD Of ECE T.Chandra sekhar garu and HOD Of EEE B.Vamsi krishna garu and all the teaching staff of Electronics and Communication Engineering, Electronics and Electrical Engineering.

On the same day Session was taken by the Resource person .P.Gangadhar has given introduction about digital signal processing and VLSI for Health Monitoring Systems.

On the 2nd day session was started at 10AM and the resource person given his lecture on VLSI for Wireless application, VLSI logic optimization

On the 3rd day Session was started usual time at 10AM and given the lecture on Graphical representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm, VLSI wireless application case studies: 16, Chaitanya Knowledge City representation of DSP Algorithm (No. 100 PSP) (No

On the 4th day Session was started usual time at 10AM and given the lecture on High level transformations of SFG and DFG.

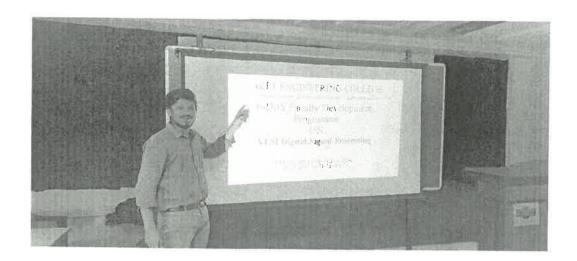
On the 5^{th} day Session was started usual time at 10AM and given the lecture on Concept of folding, Area and power minimization.

At the end of the session in valedictory function, resource person felicitated by the Principal and HOD's of all departments in GIET Engineering College.

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Resource person Demonstrating about VLSI Digital Signal Processing

B. U. L.

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TEL: 91-883-6577446, 9133833322, http://www.gietec.ac.in Email: principal t9@yahoo.co.in

6-Day FDP on VLSI Digital Signal Processing

Attendance Sheet

		ATTENDANCE							
S.NO	NAME OF THE FACULTY	06-11-2017	07-11-2017	08-11-2017	09-11-2017	10-11-2017	10-11-2017		
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NAME OF THE FACULTY	ATTENDANCE						
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G. Nalini	G. Nalinia	G. Nalinis	G. Nalinis	G. Naling	G. Nalinia	G. Nalinie	
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M. Sumanjali	M. Surney	M. Sumonja	M. Sumavjal	M-Sumaripl	i M. Sumonjali	M. Sumonijali	
	MS. Mungasi. Priyadassini S. Durga Prasad S. Sowjanya MY. E. Mothan Ganesh G. Vara Lakshmi p. Grangaden T. Balu G. Nalini T. Chardra Sekar R. Prasad foi B. Vama krush S. Divya P. Sine krish K. mamatha	MS. Mungasi. Priyadassini M. P.dorsini S. Durga Prasad S. D. Bajal S. Sowjanya S. Sowjanya MY. G. Moltan Ganesh G. M. Ganesh G. Vara Lakshmi G. V. Jakhini b. Grangaden f. Grangades T. Balu T. Kalue G. Nalini T. Charitan Sekar P. Malinis T. Charitan Sekar P. Majad B. Vama Kauhn S. Divya S. Dize P. Sive Krisha K. mamatha K. mamatha	MS. Mungasi. Priyadassini M. P. darsini M. P. darsini S. Durga Prasad S. D. Bayal J. D. Bayal S. Sowjanya S. Sowjanya S. Sowjanya MY. E. Mothan Ganesh E. M. Ganesh E. M. Granesh G. Vara Lakshmi G. V. faliphi, G. V. faliphi p. Grangades J. Grangades T. Balu T. Balue T. Balue G. Nalini T. Chartea Sekar. Prasad R. Prasad R. Prasad foi R. Nagad R. Prasad B. Vama: kowh. S. Divia P. Sive krish. K. mamatha K. mamatha K. mamatha	MS. Mungosi. Priyadassini M. P. davin S. Sowjanya 6. M. Ganeth C. M. Ganeth G. V. Jakyhi P. Grangader P. Grangader P. Grangader P. Grangader P. Grangader P. Balu T. Balue G. Nalini R. Prasad R. S. Divya R. Sinza Krijsha K. mamatha K. mamatha K. mamatha K. mamatha K. mamatha	MS. Mungasi. Priyadassini Mpdassini M. p. dassini M. p. da	MS. Mungasi. Priyadassini Mpderrin M. P. dersin M. Dersin M. P. dersin M. P. dersin M. P. dersin	

Principal GIET ENGINEERING COLLEGI NH-16, Chaitanya Knowledge City. RAJAHMUNDRY-533 296.

S.NO	NAME OF THE FACULTY	ATTENDANCE							
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25	L. YOG71	1. 4691	L. yegi	L. yogi	L. yogi	L. Yogi	L.yogi		
26	G. VENKATESWARA RAO	OVR	GR	GVA	COVIS	GVA	GNR		
27	C. VEERENDRA KUMAR.	1			4	Civeralia	C. Vesples.		
28	P. Tulga Venkatalakshmi	P. D.V. Jakhar	22. Valeton	P.DV Jakhn	P.D. Valeni	20 V Jakhni	P.D.V Jakon		
29	S.v. Sailaia					s.v. Sailoig	,		
30	B. Sund Kumar		A			B. Sun Kuma			
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