

NH - 16, Chaitanya Knowledge City, GIET Campus, Rajamahendravaram, Andhra Pradesh - 533296 Website: http://www.gietec.ac.in Email: principalgeo@giet.ac.in

CIRCULAR

Date: 24/10/2020

ORIGINATOR	ECE & EEE
CIRCULATED TO	Department of - CE, EEE, MECH, ECE, CSE, MBA, HBS, LIBRARY.

All faculty and students are hereby informed that the departments of ECE and EEE organizing a 5 day FDP on **VLSI Design and Modeling** from 02/11/2020 to 06/11/2020 in GIET ENGINEERING COLLEGE, Rajahmundry at Seminar Hall, C.V Raman Block.

We are requesting all faculty members to attend the session to make it grand success.

ENEERIN

METORAL

Venue: Seminar hall, C.V Raman Block

Date of programme: 02/11/2020

PRINCIPAL

Principal

GIET ENGINEERING COLLEGE, NH-16, Chaitanya Knowledge City, RAJAHMUNDRY - 533 296.

Principal
GIET ENGINEERING COLLEGE,
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

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FIVE DAYS FACELTY
DEVELOPMENT PROGRAMME
ON VLSI Design and

Modeling

ORGANIZED BY



DATES: 02-11-2020 (TQ) 06-11-2020

CONEVENOR

Dr. B. JAGADEESH (Dept. of ECE)

COORDINATOR
Dr. B. VAMSI KRISHNA
(Dept. of EEE)

GIET ENGINEERING COLLEGE.

GIET Engineering College was established in 2005 by Chantanya Group & located at Chantanya knowledge city on NH-5 on the outskirts of Rajahmundry in the GIET Campus GIET Engineering College is an epitome of excellence in every aspect. Approved by AICTE and affiliated to INTU Kakirasda, the institute offers undergraduate programs in diverse engineering streams. It features state of the art fadilities and highly qualified and knowledgeable pool of staff members. Apart from the prescribed course-curriculum, the institute offers an entire range of aixiliary programs aimed at providing an entire educational experience. It has been a pioneer in adopting several pedagogic innovations for the benefits of students. The achievements of the students of this institution in academics as well as placements stand destimony to the facts the Hosting Repartments.

ABOUT THE HOSTING DEPARTMENTS

The department of ECE &EEE was established in 2005 the department. Imparting of state of the art and quality education training and research in field of electronics and communication and engineering.

ABOUT THE OBJECTIVE OF FDP

In EFF branch students have to study Electrical as well as Electronics. The study and applications of electrical system for use in different environments and contain basic electronic courses and more power systems.

power systems.

EEE branch deals with electrical electronics. Electronics branch deals with electrical technology and machines, circuits, analysis and electron magnetism. Electrical engineering materials' instrumentation control system. Disver engineering transmission and distribution.

ECE aspirants deals with electronics with some part of communications. The subject uses the scientific knowledge of the behavior and effects of electronics to develop devices, systems or equipment devices that uses electricity as part of its driving forces. Also it contains basics on electric machines, on integrated circuits and communications system.

ELIGIBILITY:

The program is open to all the students of AICTE approved engineering colleges And faculties.

A five days faculty development programmed on VLSI Design and Modelling (02/11/2020 to 06/11/2020) GIET ENGINEERING COLLEGE.

Department of ____

- 1. Name:
- 2. Date of Birth:
- 3. Branch:
- 4. College Name:
- 5. Address for Correspondence with e-mail and phone no. :

Declaration:

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the course and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinators in case, I am unable to attend the course.

Date:

Signature of the applicant

Online Registration:

led.gite@gmail.com

Resource persons for this program: B.venkatesh,Asst.Prof,ISTS Womens Engineering college,East Gonagudem The resource persons those who will be conducting the program are the eminent academicians from reputed institutions.

Registration Fee: Rs. 250/- per participant.

Chief Patron

Dr.K.V.V.Satyanarayana Raju Chairman, Chaitanya Group of institutions.

Sri K.Sasi Kiran Varma vice charman, GIET . institutions

Smt.K.Lakshmi Sasi Varma
Executive Director, GIET institutions
Co-Patrons
Dr. D.L.N.Raju CEO, GIET institutions

Dr. D.L.N.Raju CEO, GIET institutions Dr. P. Subba Raju General Manager, GIET institutions.

Principal Dr.T.Vamsi kiran Principal, GIET engineering college



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Department of ECE & EEE

5-Day FDP on VLSI Design and Modeling

SCHEDULE SHEET

		SCHEDULE SHEET					
DATE	TIME	TOPIC					
	10 AM TO 11.30 PM	Inauguration					
DAY1	11.30AM TO 11:45PM	Break					
	11:45AM TO 1:00 PM	Introduction					
	2PM TO 4.30 PM	Evolution of Electronics & VLSI Design.					
DAY2	10 AM TO 1PM	VLSI- Monolithic and Hybrid IC					
	2PM TO 4.30 PM	VLSI Design isolation, Oxide isolation, Diode Isolation, Building blocks, Layout of Passive element.					
	10 AM TO 1PM	Resistor geometry, Power dissipation, Power density, Tolerance.					
DAY3	2PM TO 4.30 PM	Design of semiconductor diffused resistors, thin film resistor and Monolithic Capacitor.					
DAY4	10 AM TO 1PM	Analytical modelling of MOS Devices in VLSI, Deep sub- micron modelling, Advance MOSFET Design Structure					
	2PM TO 4.30 PM	Design of Silicon Integrated circuits, Concept of buried layer, Initial Artwork, Isolation Diffusion					
DAY5	10 AM TO 1PM	CMOS Digital IC Design, Threshold Voltage of Digital IC, Inverter Design					
	2PM TO 4.30 PM	Valedictory Waledictory Output GIET ENGINEERING COLLE NH-16. Chaitanya Knowledge RAJAMMUNDRY-533 298					

CONVENOR



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5-Day FDP on VLSI Design and Modeling.

Attendance Sheet

		ATTENDANCE					
SNO	NAME OF THE FACULTY	02-11-2020	03-11-2020	04-11-2020	05-11-2020	06-11-2020	
1	kotikalapudi shiva sankar	Shiveshankar	Shivashankar	Shouashankaz	shoushankar	Shouashankar	
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5	Rubia Tasneem	Rranem	R. Talney	R. Parneems		R. Tasnemy	
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7	T. Vishnu Murthy	Murstry	Murany	Munths	mothy	Murtty	
8	Pidugu Krishna chaitanya	P.K. datlga	Pl Clothya	P. M. clentar	P. K. Chatage	PK claseye.	
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Principal
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SNO	NAME OF THE FACULTY	ATTENDANCE						
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10	D.V Venkata lakshmi	p. Johns	P. late	D. lake	O. later	D. Jakk		
11	Srinivas Mattaparthi	Srimbo	Skinuber	S91/1000.	Solvager	SAIVAD		
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13	p. Ramya Krishm	P. Ramyakiah.	P. Keinyaksish	P. Ranjakoush	P. Ranjakoushe	P-Rangakashi		
14	p. venkatesh							
15	p. Anuradha	P. Armadla	8. Amorala	P. Ahuralla	9 Aburalla	P. Venkalch P. Annadha		
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			7				
	Principal						

Principal
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Signature of the Convenor



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5-Day FDP on VLSI Design and Modeling Date: 02/11/2020

Programme Objectives

In the last few years, VLSI Design Technologies have gained immense popularity in the research community. The aim of this FDP is to provide research aspects and solutions to the various problems related to the VLSI Design & Modelling.

- 1. To provide a comprehensive overview of the fundamental concepts as well as latest CAD TOOLS (Mentor Graphics & Silvaco).
- 2. Focus on emerging concepts of VLSI Design & Modelling.
- 3. To expand the in-depth knowledge of faculties, industry delegates.

4. To generate more open problems for research in related areas.

S.) ~ / S CONVENOR

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Summary Report

Date: 08/11/2020

Description:

5-Day FDP Programme on "VLSI Design and Modeling" was conducted for the academic staff of several colleges, including GIET Engineering College, and will run from 02/11/2020 to 06/11/2020. The FDP's resource person was B.Venkatesh, Department Of ECE, Assistant professor, ISTS Womens Engineering College. Faculty members have gained in-depth knowledge of upcoming demands in many research fields, criteria to be taken into account when publishing papers in reputable journals, and various cutting-edge teaching techniques.

On 02-11-2020 function was inaugurated by the chairman sri.K.V.V.Satyanarayana Raju garu, Principal Dr.T.Vamsee kiran garu, HOD Of ECE Dr.B.Jagadeesh and all the teaching staff of ECE &EEE.

On the same day Session was taken by the Resource person .B.Venkatesh has given introduction about.

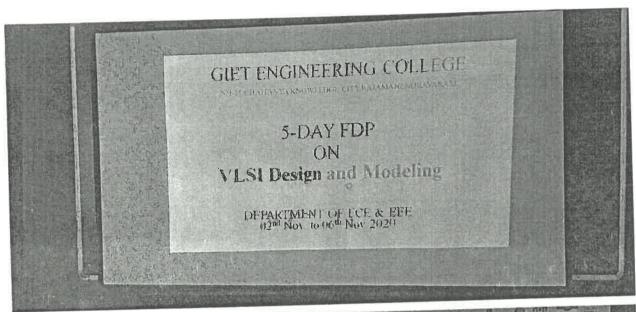
On the 2nd day session was started at 10AM and the resource person given his lecture on objectives of VLSI Design isolation, Oxide isolation, Diode Isolation, Building blocks, Layout of Passive element.

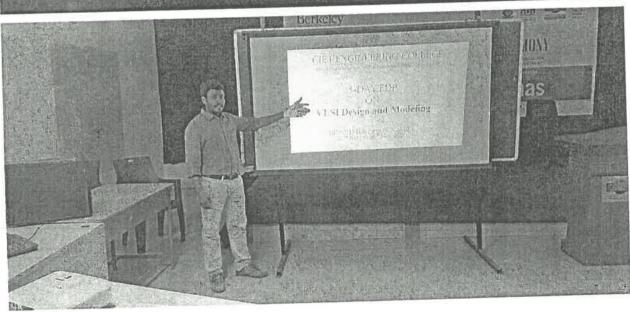
On the 3rd day Session was started usual time at 10AM and given the lecture on Design of semiconductor diffused resistors, thin film resistor and Monolithic Capacitor

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Resource person Demonstrating about VLSI Design and Modeling

PAJAMAHE

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ORIGINATOR	CSE & IT
CIRCULATED TO	Department of – CE, EEE, MECH, ECE, CSE, MBA, HBS, LIBRARY.

Dt: 02/02/2022

CIRCULAR

All staff and students are hereby informed that the departments of CSE and IT organizing a 5-Day FDP on AI & ML from 07/02/2022 to 11/02/2022 in GIET ENGINEERING COLLEGE, Rajahmundry.

As a result, we invite all interested faculty members to attend the sessions and help us make it a huge success.

Copy to:

1. All the HODs

2. E-Circulation to among all the staff.

3. All notice boards

4. A.O

5. File

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RAJAHMUNDRY - 533 296.

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ABOUT COLLEGE

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ABOUT DEPARTMENT

Department of Computer Science and Engineering was started in the year 2005. It is offering graduate program B.Tech. Department has well qualified and dedicated faculty members. Department has produced good number of graduates well settled in industry and some are also involved in research activities

ABOUT PROGRAM

Artificial intelligence and machine learning are two popular and often hyped terms these days. And people often use them interchangeably to describe an intelligent software or system problem.

This course includes examples of AI & ML and algorithms of various types of ML techniques.

This FDP helps faculty and participants to share their ideas and gain the knowledge in the area of Artificial Intelligence.

Program Contents

- Fundamentals of AI & ML
- Concepts of AI & ML
- Real time environment on ML
- Regression Algorithms
- Concepts of ANN

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5-DAY
FACULTY DEVELOPMENT
PROGRAM
ON
AI & ML

07th to 11th FEB 2022

Organized by

Department of
Computer Science and
Engineering



GIET ENGINEERING COLLEGE



Registration form

5-DAY
FACULTY DEVELOPMENT PROGRAM
ON
AI & ML

Name of the Participant:

Name of the Institute:

Address for Correspondence:

Department:

Mail Id:

Phone no:

Signature of the participant:

Dt:



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Sri. Sasi Kiran Varma, Vice Chairman, Chaitanya

Group

Smt. K.V.V .Lakshmi, Executive Director, Chaitanya

Group

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CHAIRMAN

Dr. T. Vamsi Kiran ,Principal, GEC

CONVENER

Dr. SK. Meera Sharief, HOD CSE

CO-CONVENER

Mr.P.V.S.K. Manikanta, Assistant .Prof

RESOURCE PERSON

Dr. A. Radha Krishna

Associate Professor, Dept. of CSE

Pragathi Engineering College

ORGANISING COMMITTEE

Mr. B.Srinivasa Rao, Ass. prof

Mr. J.Bala Ambedkar, Ass. prof

Mrs. AS.Dhana Lakshmi, Ass.prof

CONTACT:

Mr B.Srinivasa Rao,9502633549

Srinicas.cse@giet.ac.in

Mr.J. Bala Ambedkar, 9490912524

Bala.jbs@gmail.com

REGISTRATION FEES: 500Rs

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NH-16, CHAITANYA KNOWLEDGE CITY-RAJAHMUNDRY

DEPARTMENT OF CSE & IT

5-DAY FACULTY DEVELOPMENT PROGRAM ON AI & ML

ATTENDANCE SHEET

S.NO	NAME OF THE FACULTY	07-2-22	8-2-22	9-2-22	10-2-22	11-2-22
1	VEERENDRA BETHINEEDI	VAR	MAR	VACA	AD	FQ-
2	SHIVA N V RAMANA MURTHY BANDA	Kanr	Ramm	Ram	Rama	Rama
3	RAMANNADORA ORUGANTI	Ofmo	ORun	OR OR	OR	OR.
4	APARNA KANCHIMENU	Arama	Daring	Aporna	Awma	Arama
5	SUREKHA CHODISETTI	Swuka	Swieka	Swreka	Swuka	Swaka
6	ANJALI SATYAVATI YERRA	V. Asubjer	VA satya	V-A. Wya	V.D. Salzya	Mrs - Saly
7	RAMYA SETTI	Siperso	C. Rouge	C Renje	2 Permis	Shinds
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9	A. JANARDHANA RAO	Ras	Rat	Red	Rad	600
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13	G. UMA MAHESH	Mahesh	Maketh	Mahish	Kaherh	Mahak

14	T. DURGA	T. \$	T. D-	TID	T.D-	7.5
15	K. SHAMSRI	Soi	Soi	Si	Sri	Soi
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17	K. SATISH KUMAR	KISIK	K&K	K-s-le	Krsrk	KILK
18	VEMAGIRI PREM KUMAR	V. Kuma	V. Kund	V. Kund	V. Kunt	V. Kust.
19	D. AISWARYA	A	A	a)	a	A
20	G.NAGARANI	A	A	A	A	A
21	CHINTHA VENKATA RAMANA	R	R-	P_	Q_	R
22	K.V.V.S. TRINADH NAIDU	K. Naidu	K. Naider	K-Naidu	K. Naidu	K. Naide
23	CH.N.L. GEETHA	Galle	Gette	Galle	Geelle	Ges
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30	BANDA MALLESHAM	B.A.	BA	B. A.	B.A.	B. W
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NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296.

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35	J.BALA AMBEDKAR	J.D.CA	J.BA	J.MA	J.B.A	J.B.Q
36	P.V.S.K.A.MANIKANTA	Manikat	Manikat	Manukat	Manifemte	maninkel
37	CH. ARCHANA	A	R	Q.	R	R
38	G.SIVA KRISHNA	Straderisho	Swarkeigh	Sivakrish	Sinkul	Strakelshe
39	T. MEENAKSHI	-Cm	Top	- (Th)	P	(Tm).
40	K.V.V.SIVA PRASAD	kw.prud	Kn. gund	kupund	Kripared	k-v praced
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pv.g.b. Mani co-convener

Principal
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5-DAY FDP ON AI & ML

Department of Computer Science and Engineering

PROGRAM REPORT

Dt: 14-02-2022

Resource Person

: Dr. A Radha Krishna

Assoc.Professor

Department of CSE

Pragati Engineering College

Duration

: 5-Days

Dates

: 07/02/2022 to 11/02/2022

No.Of Participants

: 40

Description

: On 07-02-2022 function was inaugurated by the chairman sri.K.V.V.Satyanarayana Raju garu, Principal Dr.T.Vamsi Kiran, HOD Of CSE Dr.Sk.Meera Sharief and all the teaching staff of Computer Science and Engineering.

On the same day Session was taken by the Resource person given an introduction on AI, Overview of AI.

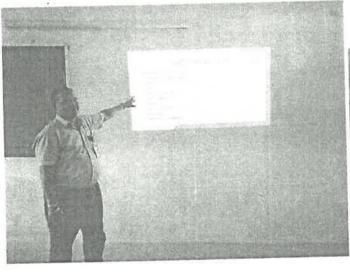
On the 2nd day session was started at 10AM and the resource person given his lecture on intelligent system and AI Agents and Environments.

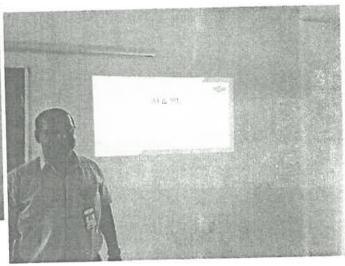
On the 3rd day Session was started usual time at 10AM and given the lecture on Introduction of Machine Learning and types of ML and its applications.

GIET ENGINEERING COLLEGE NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296. On the 4th day Session was started usual time at 10AM and given the lecture on Supervised and Unsupervised Learning Algorithms.

On the last day of the event and given the lecture on Introduction of Artificial Neural Networks.

At the end of the session in valedictory function, resource person felicitated by the Principal and HOD's of all departments in GIET Engineering College.





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Convener



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Certificate

This is to certify that Dr/Mr/Mrs. T. DEEPTHI

Of Pragati Engg College.

participated in a Five Day Faculty Development Program on "AI & ML" held from 7th to 11th

Feb 2022, Organised by Department of Computer Science and Engineering, GIET Engineering College.

Co-ordinator

GIET ENGINEERING COLLEGE. NH-16, Chaltanya Knowledge City, RAJAHMUNDRY-533 296.

Principal Principal



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Certificate

This is to certify that Dr/Mr/Mrs. U. VINOD KUMAR

Of GVET Collège of Engineering

participated in a Five Day Faculty Development Program on "AI & ML" held from 7th to 11th

Feb 2022, Organised by Department of Computer Science and Engineering, GIET Engineering College.

3. sin 2.

Co-ordinator

Principal
GIET ENGINEERING COLLEGF.
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Principal



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Certificate

This is to certify that Dr/Mr/Mrs.

TEJASVI

Of Pragali Engg College

participated in a Five Day Faculty Development Program on "AI & ML" held from 7th to 11th

Feb 2022, Organised by Department of Computer Science and Engineering, GIET Engineering College.

3. s. - Dr.

Co-ordinator

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Date: 13/12/2021.

ORIGINATOR	ME & CIVIL ENGINEERING
CIRCULATED TO	Department of - CSE, IT, ECE, EEE, MBA, HBS,
	LIBRARY,

All staff are hereby notified that the departments of ME & CIVIL will be holding a 5 Days Faculty Development Program on "ENGINEERING GRAPHICS" From 13/12/2021 to 17/12/2021 , everyday from 10:00 AM to 4:30 PM , at the C.V Raman Block Seminar Hall

GINEERING

AHENDRAY

Venue: C.V Raman Block Seminar Hall

Date: 13/12/2022

Copy To:

1.All HoDs(for information)

2. Notice Board, IQAC

3.File

Phrcipal
GIET ENGINEERING COLLEGE.
NH-15, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

Principal
GIET ENGINEERING COLLEGE,
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

A Five Days Faculty Refresher Course

On

"ENGINEERING GRAPHICS"

13th-17th December 2021

REGISTRATION FORM

Name

:

Designation:

Department:

Institution/:

College

Address

Contact No :

E-mail ID

Place: Date:

Applicant's Signature

Registration: Participants have to send their registration form to the coordinators in the prescribed format duly attested by the Head of the Institution.

REGISTRATION FEE:

Fee -300/-

CHIEF PATRON

Sri. K.V.V. Satyanarayana Raju

Chairman, GIET Institutions

PATRON

Sri. Sasi Kiran Varma Kalidindi

Vice Chairman, GIET Institutions

Smt. Lakshmi Sasi Varma Kalidindi

Executive Director, GIET Institutions

Dr. P. Subba Raju,

General Manager, GIET Institutions

Dr. T.V. Prasad,

Director QA, GIET Institutions

CHAIRMAN

Dr. M.V.S. Babu

Professor of ME & Principal GEC

Coordinator

E.T. Chakrapani,

Assot.Prof (Ph.D)

Head, Civil Engg. Dept.

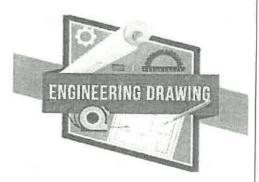
Convenor

Pinjarla Poorna Mohan,

Sr. Asst.Prof (Ph.D)

Head, Mechanical Engg. Dept

GIET ENGINEEHING COLLEGI NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296. A 5-Day Faculty Refresher Course on "ENGINEERING GRAPHICS" 13th-17th December 2021



Jointly Organized
by

Mechanical Engineering Department
&
Civil Engineering Department



GIET Engineering College NH-16, Chaitanya Knowledge City, Rajahmundry-533 296, EAST Godavari District, Andhra Pradesh, India

ABOUT THE COLLEGE:

The college is located at Chaitanya Nagar on NH-16 on the outskirts of Rajamahendravaram, in GIET campus. GIET Engineering College is an epitome of excellence in every aspect. Institute is Approved by AICTE and affiliated to JNT University Kakinada (JNTUK).

The Institute got accredited by NAAC. The institute offers undergraduate programs in diverse engineering streams. It features state-of-the-art facilities and highly qualified and knowledgeable pool of staff members.

Apart from the prescribed course curricula, the institute offers an entire educational experience with greater exposure of students advanced skill learning via existing centres of excellence. It has been a pioneer in adopting several pedagogic innovations for the benefit of students. The achievements of the students of this institution in academics as well as placements stand testimony to the fact.

The institute is offering 4 UG programmes in engineering with 360 intake, 1 PG programmes including MBA & MCA.

B.Tech. Mechanical Engineering

B. Tech. Electronics Communication Engineering

B.Tech. Information Technology

B.Tech. Computer Science Engineering

B.Tech. CSE(Artificial Intelligence and Machine Learning)

B.Tech. CSE (Cyber Security)

B.Tech. CSE (Data Science)

M.B.A.

M.C.A.

ABOUT THE DEPARTMENT:

The Department of Mechanical Engineering is one of the leading departments in the Institute, with highly qualified full-time faculty members and full-time administrative and technical support staff. The department offers B.Tech., M.Tech., and Ph.D. degree programs, with over 600 undergraduate, 40 M.Tech. students on roll. The department has over 20 instructional and research laboratories, equipped with some of the state-of-the-art scientific instruments, latest software's and systems. The vision of the department is to be recognized as a center of excellence in Mechanical Engineering education, research and consultancy. To achieve this goal, the department strives to engender high quality graduates through scrupulous courses and progressive research activities. The students in the department are encouraged to take contemporary the problems faced by industries and society. Students are educated to deal with those problems through innovative thinking to develop appropriate technological solutions. In addition to the regular academic work, the students of the department are encouraged to participate in cocurricular activities. Research interest of the faculty members Design Engineering, Manufacturing Engineering and Thermal Engineering.

Civil Engineering is a good choice for students who want to work directly with society and serve it. The primary purpose of any Civil Engineer is to provide various facilities and build necessary infrastructure to meet all social needs.

GIET ENGINEERING COLLEGI NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296.

ABOUT THE PROGRAMME:

Engineering drawing, most commonly referred to as engineering graphics, is the art of manipulation of designs of a variety of components, especially those related to engineering. It primarily consists of sketching the actual component, for example, a machine, with its exact dimensions, and using entities such as points, lines, arcs, etcetera. The scale of dimensions is suitably adjusted so as to properly fit within the contours of the drawing sheet.

TOPICS TO BE COVERED:

- Introduction to Engineering Graphics
- Projections of Points
- > Projections of Lines
- Projections of Planes
- > Projections of Solids
- Orthographic Projections
- Isometric Projections
- Computer Aided Design

RESOURCE PERSONS:

1. I.KRISHNA CHAITANYA,
ASSOC PROFESSOR,
DEPARTMENT OF CIVIL ENGINEERING,
RAGHU ENGINEERING COLLEGE.



NH - 16, Chaitanya Knowledge City, GIET Campus, Rajamahendravaram, Andhra Pradesh - 533296 Website: http://www.gietec.ac.in Email: principalgec@giet.ac.in

DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program **ENGINEERING GRAPHICS**

SCHEDULE SHEET

DAY	TIME	TOPIC
	10:00 TO 11:30	Inauguration
DAY 1	11:30 TO 11:45	Break
	11:45 TO 01:00	Introduction
	01 : 00 TO 04 : 30	Introduction to Engineering Graphics
DAY 2	10:00 TO 01:00	Projections of Points
	02:00 TO 04:30	Projections of Lines
DAY 3	10:00 TO 01:00	Projections of Planes
	02:00 TO 04:30	Projections of Solids
DAY 4	10:00 TO 01:00	Orthographic Projections
	02:00 TO 04:30	Isometric Projections
DAY 5	10:00 TO 11:00	Computer Aided Design
2111 5	11:00 TO 12:30	valedictory

HGINEERING

CONVENOR

PRINCIPAL Principal

ENGINEERING COLLEGE 16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296.



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DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program on ENGINEERING GRAPHICS

Teaching Staff Attendance Sheet

S.NO	NAME OF THE FACULTY	13/12/21	14/12/21	15/12/21	16/12/21	17/12/21
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Principal COLLEGE, PRINCIPAL GINEERING COLLEGE, Principal Principal Principal Chaitanya Knowledge City, Principal NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296.



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DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program on ENGINEERING GRAPHICS

Teaching Staff Attendance Sheet

S.NO	NAME OF THE FACULTY	13/12/21	14/12/21	15/12/21	16/12/21	17/12/21
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CONVENOR

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Principal
GINEERING COLLEGET ENGINEERING EN



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DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program on ENGINEERING GRAPHICS

OBJECTIVES

The ultimate aim of this FDP is to train the beginners to the teaching community of academia and

- > To impart and inculcate proper understanding of the theory of projection.
- > To improve the visualization skills.
- To enable with various concepts like dimensioning, conventions and standards related to working drawings in order to become professionally efficient.
- > To impart the knowledge on understanding and drawing of simple mechanical components.

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DEPARTMENT OF ME & CIVIL ENGINEERING

5 DAYS Faculty Development Program on ENGINEERING GRAPHICS

Venue: C.V Raman Block Seminar Hall

Date: 18/12/21

"Summary Report"

Description:

The 5-day FDP programme on "ENGINEERING GRAPHICS"

was conducted for the academic staff of several colleges, including GIET Engineering College, and will run from 13-12-2021 to 17-12-2021. The FDP's resource person was I.Krishna Chaitanya ,Assoc Professor,Department Of Civil Engineering ,Raghu Engineering College. Faculty members have gained in-depth knowledge of upcoming demands in many research fields, criteria to be taken into account when publishing papers in reputable journals, and various cutting-edge teaching techniques.

Programme Outcomes:

- 1- Get information about the important tools for engineering drawing. This will give student basic knowledge of technical drawings professions and means of communications to others.
- 2- Learning how to draw the shapes, angels and lines and others which is essential for engineer
- 3- Develop student's imagination and ability to represent the shape size and specifications of physical objects.
- 4- Understand the main idea of using dimension for engineering drawing
- 5- Familiarize with different drawing equipment, technical standards and procedures for construction of geometric figures. This will give students ability to draw three dimension objects on the paper and to draw the pectoral drawings.
- 6- Explain the principle of projection and sectioning
- 7- Understand the intersection, development of surface of body and fasteners

8- Learning the main idea from assembly and detail drawing

COVENOR

ENGINEERING COLLEGE,
Chaitanya Knowledge City,
IAHMUNDRY-533 296.

PRINCIPAL Principal

GIET ENGINEERING COLLL NH-16, Chaitanya Knowledge City,

RAJAHMUNDRY-533 296:



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Resource person demonstrating about Engineering graphics

CONVENOR

ARLAMAHENDRA HER

PRINCIPAL

Principal

GIET ENGINEERING COLLEGE

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PRAJAMMUNDRY-533 296.

GIET ENGINEERING COLLEGI NH-16, Chaitanya Knowledge City, RAJAHMUNDRY-533 296,



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Certificate of Appreciation

This is to certify that Mr/Ms/Dr. V. Avinos/ A 5 -Day Faculty Development Program on " ENGINEERING GRAPHICS 13 th -17th December 2021, organized by Civil Engineering Department Engineering Department, GIET Engineering College.	** * * *	rticipated from Mechanical
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Convenor

Principal
GIET ENGINEERING COLLEGI
NH-16, Chaitanya Knowledge City,
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Principal



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Certificate of Appreciation

This is to certify that Mr/Ms/Dr. S.K. Nayeem has participated A 5-Day Faculty Development Program on " ENGINEERING GRAPHICS " held from 13 th -17th December 2021, organized by Civil Engineering Department and Mechanical Engineering Department, GIET Engineering College.

Convenor

Principal
GIET ENGINEERING COLLEGE,
NH-16, Chaitanya Knowledge City,
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Principal



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CIRCULAR

Date: 12/09/2021

ORIGINATOR	ECE & EEE
CIRCULATED TO	Department of - CE, EEE, MECH, ECE, CSE, MBA, HBS, LIBRARY.

All faculty and students are hereby informed that the Departments of ECE and EEE are conducting a Five Day FDP on Image Processing from 20/09/2021 to 24/09/2021, From 10:00 AM to 4:30 PM, at Seminar Hall, C.V Raman Block.

We are requesting all faculty members to attend the session to make it grand success.

Venue: Seminar hall, C.V Raman Block

Dateof programme: 20-09-2021

PRINCIPAL

Principal
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NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY - 533 296.

Principal
GIET ENGINEERING COLLEGE
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

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Dr. B. VAMSI KRISHNA

(Dept. of EEE)

GIET ENGINEERING COLLEGE.

GIET Engineering College was established in 2005 by Chaitanya Group & tocated at Chaitanya knowledge city on NH-5 on the outskirts of Rajalimundry in the GIET Campus. GIET Engineering College is an epitome of excellence in every aspect. Approved by AICTh and affiliated to JNTU Kakinada, the institute offers undergraduate programs in diverse engineering streams. It features state- of the art facilities and highly qualified and knowledgeable pool of statT members. Apart from the prescribed ooarse curriculum, the institute offers an entire range of auxiliary programs aimed at providing an entire educational experience. It has been a pioneer in adopting several pedagogic innovations for the benefits of students. The achievements of the students of this institution in academies as well as placements stand testimony to the facts the Hosting Departments.

ABOUT THE HOSTING

The department of ECE & FEE was established in 2005 the department. Imparting of state of the art and quality education training and research in field of electronics and communication and engineering.

ABOUT THE OBJECTIVE OF FOR

In EEF branch students have to study Electrical as well as Electronics. The study and applications of, ejectrical system for use in different environments and contain basic electronic courses and more power systems.

power systems.

EEE branch deals with electrical ejectropies. Electronics branch deals with electrical ejectropies. Electronics branch deals with electrical fechnology and machines, circuits analysis, and electro magnetism. Electrical engineering materials' instrumentation control system, power engineering, transmission and distribution.

F.CE aspirants deals with electronics will spine part of communications. The subject uses the scientific knowledge of the behavior and offices if electronics to develop devices, systems for entirement devices that uses electricity as part of its driving forces. Also it contains basics on electric machines, on integrated circuits and communications system.

ELIGIBILITY:

The program is open to all the students of AICTE approved engineering colleges And faculties.

A five days faculty development programmed on IMAGE PROCESSING (20/092021 to 24/092021) GIET ENGINEERING COLLEGE.

Department of

- 1. Name:
- 2. Date of Birth:
- 3. Branch:
- 4. College Name:
- 5. Address for Correspondence with e-mail and phone no.:

Declaration:

The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the course and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinators in case, I am unable to attend the course.

Date:

Signature of the applicant

Online Registration: led.gite@gmail.com

Resource persons for this program: D.Ramesh, Asst. Prof
KITS Engineering college, Divili
The resource persons those who will be conducting the program are the eminent academicians from reputed Institutions.

Registration Fee: Rs. 250/- per participant.

<u>ChiefPatron</u> Dr.K.V.V.Satyanarayana Raju

Chairman, Chaitanya Group of institutions.

Sri.K.Sasi Kiran Varma vice charman, GIET. Institutions

Smt.K.Lakehmi Sasi Varma Executive Director, GIET institutions Co-Patrors

Dr. D.L.N.Raju CEO, GIET institutions Dr. P. Subba Raju General Manager, GIET institutions

Principal Dr.T.Varnsı kiran Principal, GIET engineering ccilege



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FIVE DAY FDP SCHEDULE

TOPIC: IMAGE PROCESSING

DATE	. TIME	TOPIC
	10 AM TO 11.30PM	Inauguration
	11.30AM TO 11:45PM	Break
DAY1	11:45AM TO 1:00PM	Introduction
	2PM TO 4.30 PM	Mathematical tools for Image processing
	10 AM TO 1PM	Morphological Image processing compression
DAY2	2PM TO 4.30 PM	Image mining
	10 AM TO 1PM	Computer vision and Image processing
DAY3	2PM TO 4.30 PM	Digital mammographic analysis
	10 AM TO 1PM	Iris pattern reorganization
DAY4	2PM TO 4.30 PM	Multimodal biometrics
	10 AM TO 1PM	Medical image processing
DAY5	2PM TO 4.30 PM	Valedictory

CONVENOR

Principal
GIET ENGINEERING COLLEG
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CO CONVENOR



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5-Day FDP on IMAGE PROCESSING

Attendance Sheet

				ATTENDANCI	Ε	
SNO	NAME OF THE FACULTY	20-09-2021	21-09-2021	22-09-2021	23-09-2021	24-09-2021
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5	K. Sri Ramwithy	K. Ramwethy	V_	Kramwithy		k. Ramwithy
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Principal
GIET ENGINEERING COLLEGE
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

SNO	NAME OF THE FACULTY	ATTENDANCE				
8	K. V.S. Akhil	Aklül	Aklin	Afhil	Afchil	Achil
9	G. Raju	Carpage	G. Rag?	Cr. Royer	Or-Rafu	Cr. Roger
10	B. K. Chakravarthy	Bke	Bke	BRC	BKL	BRe
11	G. Vinutna vjwala	CMM	Com	Curs	Crys	- Cino
12	K. Rambabu	K.Rambabu				K. Rambabu
13	B Mani Babu	Branipolis	B. Monibalu	B. Manilidu	B. Manikalen	B. Manibaby
14	5. Vaishnavi	Jarshuni	Varshur	Vaishum	Vaishum	Cashinari
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Principal
GIET ENGINEERING COLLEGE
NH-16, Chaitanya Knowledge City,
RAJAHMUNDRY-533 296.

SNO	NAME OF THE FACULTY	. ATTENDANCE				
20	M. pavani	M. Rauani	M.Buani	M. formani	M. pauani	M. paveini
21	N. Lakshmi Kalyani	N. Kalyin	N. Kalyn	N. Kelyen	N. Kalejani	W-Kaleyani
22	y. pavankumar	Y.f. Kumar		Y. P. Kumo		y.p.kumar
23.	P. Siva koishna	frishme	Krish	Korsha	Krishna	Kerlshia
24.	B. Venkatech.	B. vontills	B. ventu	B. Venki	B. Ver	B. Venkel
25	A. Kar Lhik Raju	Rju	Raja	Rajn	Raija	Rayn
26	Gr. Stylana	Gr. Soringen_	Сн. Яниуст	G. Srajus	6. Suyan_	a Soujen_
27.	B. Vamsi Krishna	Vanno	James	Vamis	Namo	Vamos
28	S-V-Sailaja	S.N Sallafon	s.v.Satlago	s.vsailer	s.v.Sailaja	s.v. sallage
29.	M. N. Saidulla.	MN. Sai			Men: San	

B. H. Signature of the Coordinator

Principal
GIET ENGINEERING COLLEGF
NH-15, Chaltanya Knowledge City,
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5-Day FDP on IMAGE PROCESSING

Programme outcomes

- 1. Familiarize with basic concepts of digital image processing and different image transforms
- 2. Learn various image processing techniques like image enhancement, segmentation and compression
- 4. Understand and morphological image processing Course Outcomes
- 5. Perform image manipulations and different image processing techniques
- 6. Perform basic operations like Enhancement, segmentation, compression, Image transforms and restoration techniques on image.
- 7. Analyze fullcolor image processing techniques.

8. Apply various morphological operators on images

CONVENOR

CO-CONVENOR

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Principal
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Summary Report

Date: 27-09-2021

Description:

5-Day FDP Programme on "Image Processing" was conducted for the academic staff of several colleges, including GIET Engineering College, and will run from 20-09-2021 to 24-09-2021. The FDP's resource person was D. Ramesh, Department of ECE, Assistant professor, KITS Engineering college, Divili. Faculty members have gained in-depth knowledge of upcoming demands in many research fields, criteria to be taken into account when publishing papers in reputable journals, and various cutting-edge teaching techniques.

Venue: Seminar hall, C V Raman Block

On 20-09-2021 function was inaugurated by the chairman sri.K.V.V.Satyanarayana Raju garu, Principal .T.Vamsee kiran Raju garu, HOD Of ECE Dr.B.Jagadeesh and all the teaching staff of Electronics and Communication Engineering.

On the same day Session was taken by the Resource person .D.Ramesh has given introduction about Mathematical tools for Image processing.

On the 2nd day session was started at 10AM and the resource person given his lecture on Morphological Image processing compression

On the 3rd day Session was started usual time at 10AM and given the lecture on Computer vision and Image processing.

On the 4th day Session was started usual time at 10AM and given the lecture on Iris pattern reorganization, Multimodal biometrics.

RAJAHMUNDRY-533 296.

On the 5^{th} day Session was started usual time at 10AM and given the lecture on Medical image processing

At the end of the session in valedictory function, resource person felicitated by the Principal and HOD's of all departments in GIET Engineering College.

CONVENOR

CO-CONVENOR

Principal
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